

# Agnisys Launches IDS-FPGA: Accelerating FPGA Development with Integrated Specification-Driven Design

*Agnisys launches IDS-FPGA, a spec-driven solution to streamline FPGA design, RTL generation, IP packaging, and system validation.*

BOSTON, MA, UNITED STATES, June 20, 2025 /EINPresswire.com/ -- Agnisys, the pioneer and industry leader in Golden Executable Specification Solutions™, announces the launch of [IDS-FPGA](#), a new product built to streamline design, verification, and implementation of FPGAs. It offers a comprehensive, user-friendly solution tailored specifically for FPGA workflows, leveraging Agnisys' decade-long expertise in register design and system integration.



## NEWS

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## A Bridge Between Specification and FPGA Realization

IDS-FPGA is an extension of Agnisys' [IDesignSpec™ \(IDS\)](#) product suite that seamlessly integrates with leading FPGA design environments, enabling designers to quickly develop high-quality, production-ready FPGA designs. By using the integrated GUI-based tool or command line interface, engineers can define register maps and sequences and auto-generate synthesizable RTL, UVM testbenches with integrated RAL models, firmware headers, programmable sequences, and comprehensive documentation—all from a single source of truth.



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*Anupam Bakshi, Founder and  
CEO of Agnisys, Inc.*

“FPGAs are becoming highly complex, IDS-FPGA fills a crucial gap in the FPGA development space,” said Anupam Bakshi, Founder and CEO of Agnisys, Inc. “It drives specification to implementation thereby drastically reducing design cycles and helping the developer come out of the lab quickly and rush to market.”

## Key Features of IDS-FPGA:

- FPGA Tool Integration: Links specification with toolchains for all popular FPGA tools and device families
- Automated IP Packaging: Generates RTL, and inserts standard bus interfaces and plumbing components into the design
- UVM/C Based Verification Environment: Automatically creates UVM/C-based [verification environments](#) and tests
- Custom Config and Test Sequences: Generates GUI-based sequences for configuration and test that run in verification environments and target boards

To learn more about IDS-FPGA and how it can accelerate your FPGA development, visit:

<https://www.agnisys.com/products/ids-fpga/>

## About Agnisys, Inc.

Agnisys is a provider of Electronic Design Automation (EDA) software and methodology services, solving complex front-end design, verification, and validation problems in system chip development. Its ISO certified IDesignSpec™ Solution Suite leverages a golden executable specification to capture and centralize registers, sequences, and connectivity for Intellectual Property (IP) and System-on-a-Chip (SoC) projects. Its intuitive user interfaces and standards-based workflows reduce risk by eliminating development errors while increasing productivity and efficiency through the automatic generation of collateral for the entire project development team. Founded in 2007, Agnisys is headquartered in Boston, Massachusetts, with R&D centers in the United States and India. Learn more at [www.agnisys.com](http://www.agnisys.com).

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